| L Number | Hits        |  | DB                     | Time stamp       |
|----------|-------------|--|------------------------|------------------|
| 1        | 1528        |  | USPAT;                 | 2003/04/14 11:47 |
|          |             | locked) or frequency adj synthesizer)  | US-PGPUB;              |                  |
|          |             |  | EPO; JPO;              |                  |
| 0        | 24          | /275/276 cala and //mll or mhace add   | DERWENT                | 2003/04/14 12:43 |
| 2        | 24          | (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and | USPAT;<br>US-PGPUB;    | 2003/04/14 12:43 |
|          |             | (power adj saving)   | EPO; JPO;              |                  |
|          |             | (power adj saving)   | DERWENT                |                  |
| 3        | 13          | ((375/376.ccls. and ((pll or phase adj   | USPAT;                 | 2003/04/14 10:54 |
| J        |             | locked) or frequency adj synthesizer)) and                                       | US-PGPUB;              |                  |
|          |             | (power adj saving)) and (transceiver or  | EPO; JPO;              |                  |
|          |             | transmitter or receiver)   | DERWENT                |                  |
| 4        | 9           | ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '  | USPAT;                 | 2003/04/14 09:57 |
|          |             | locked) or frequency adj synthesizer)) and                                       | US-PGPUB;              |                  |
|          |             | (power adj saving)) and (transceiver or  | EPO; JPO;              |                  |
| r        | _           | (transmitter and receiver))  | DERWENT                | 2002/04/14 10-55 |
| 5        | 0           | 1,1  | USPAT;                 | 2003/04/14 10:55 |
|          |             | locked) or frequency adj synthesizer)) and                                       | US-PGPUB;<br>EPO; JPO; |                  |
|          |             | (frequency adj2 divider\$1 adj rate\$1)  | DERWENT                |                  |
| 6        | 460         | (375/376.ccls. and ((pll or phase adj  | USPAT;                 | 2003/04/14 10:55 |
| J        |             | locked) or frequency adj synthesizer)) and                                       | US-PGPUB;              | 2003/04/14 10.33 |
|          |             | (frequency adj2 divider\$1)  | EPO; JPO;              |                  |
|          |             |  | DERWENT                |                  |
| 7        | 2746241     | ((375/376.ccls. and ((pll or phase adj   | USPAT;                 | 2003/04/14 10:56 |
|          |             | locked) or frequency adj synthesizer)) and                                       | US-PGPUB;              |                  |
|          |             | (frequency adj2 divider\$1)) and   | EPO; JPO;              |                  |
| _        |             | independent\$3 or (separate\$2)  | DERWENT                |                  |
| 8        | 196         |  | USPAT;                 | 2003/04/14 10:56 |
|          |             | locked) or frequency adj synthesizer)) and                                       | US-PGPUB;<br>EPO; JPO; |                  |
|          |             | (frequency adj2 divider\$1)) and (independent\$3 or (separate\$2))               | DERWENT                |                  |
| 9        | 116         |  | USPAT;                 | 2003/04/14 10:57 |
| ,        |             | locked) or frequency adj synthesizer)) and                                       | US-PGPUB;              |                  |
|          |             | (frequency adj2 divider\$1)) and   | EPO; JPO;              |                  |
|          |             | (independent\$3 or (separate\$2))) and   | DERWENT                |                  |
|          |             | frequency adj3 reference   |                        |                  |
| 10       | 73          | 1 ' ' ' ' '  | USPAT;                 | 2003/04/14 10:57 |
|          |             | locked) or frequency adj synthesizer)) and                                       | US-PGPUB;              |                  |
|          |             | (frequency adj2 divider\$1)) and   | EPO; JPO;              |                  |
|          |             | (independent\$3 or (separate\$2))) and   | DERWENT                |                  |
|          |             | frequency adj3 reference) and frequency  |                        |                  |
| 11       | 1           | adj3 input ((((((375/376.ccls. and ((pll or phase adj                            | USPAT;                 | 2003/04/14 11:13 |
| 11       | i *         | locked) or frequency adj synthesizer)) and                                       |                        | 2003/04/14 11:13 |
|          |             | (frequency adj2 divider\$1)) and   | EPO; JPO;              |                  |
|          |             | (independent\$3 or (separate\$2))) and   | DERWENT                |                  |
|          |             | frequency adj3 reference) and frequency  |                        |                  |
|          |             | adj3 input) and (controll\$3 adj   |                        |                  |
|          | _           | (independent\$2 or separate\$2))   |                        |                  |
| 12       | 58761       | frequency adj divid\$3   | USPAT;                 | 2003/04/14 11:25 |
|          |             |  | US-PGPUB;              |                  |
|          |             |  | EPO; JPO;              |                  |
| 13       | 4126        | (frequency adj divid\$3) and reference adj                                       | DERWENT<br>USPAT;      | 2003/04/14 11:15 |
| 13       | 4120        | signal\$1 and (input adj signal\$1)  | US-PGPUB;              | 2003/04/14 11.13 |
|          |             | orginaryr and (rinput da) orginaryr/   | EPO; JPO;              |                  |
|          |             |  | DERWENT                |                  |
| 14       | 1293        | ((frequency adj divid\$3) and reference adj                                      | USPAT;                 | 2003/04/14 11:15 |
|          |             | signal\$1 and (input adj signal\$1)) and   | US-PGPUB;              |                  |
|          |             | (independently or separately)  | EPO; JPO;              |                  |
|          |             |  | DERWENT                |                  |
| 15       | 37          | 1 '  | USPAT;                 | 2003/04/14 11:15 |
|          |             | signal\$1 and (input adj signal\$1)) and   | US-PGPUB;              |                  |
|          |             | (independently or separately) adj set  | EPO; JPO;              |                  |
| 16       | 167         | frequency add divided add rates1   | DERWENT<br>USPAT;      | 2003/04/14 11:30 |
| 16       | 16/         | frequency adj divid\$3 adj rate\$1   | US-PGPUB;              | 2003/04/14 11.30 |
|          |             |  | EPO; JPO;              |                  |
|          |             |  | DERWENT                |                  |
|          | <del></del> | <u> </u>   |                        | ·                |

| adj (independently or separately)  18  |     |     |   |           |                     |
|--|-----|-----|---|-----------|---------------------|
| 18   | 17  | 4   | (frequency adj divid\$3 adj rate\$1) and set                                    | USPAT;    | 2003/04/14 11:26    |
| 1  | •   |     | adj (independently or separately)   |           | 1                   |
| 13   |     |     |   |           | 1                   |
| 10cked) or frequency adj synthesizer) and (power adj saving) and frequency adj of (175/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer) and (power adj saving) and frequency adj of (power adj saving) and frequency adj (power adj saving) and control adj circuit\$1 and control adj circuit\$1 and control adj circuit\$1 and frequency adj (power adj saving) and frequency adj (power adj saving) and control adj circuit\$1 and frequency adj (power adj saving) and frequency adj (power adj saving) and frequency adj (power adj saving) and power adj saving) and frequency adj (power adj saving) and power adj saving) and power adj saving (power adj saving) and power adj saving) and frequency adj (power) and control adj circuit\$1) and frequency adj (power) adj (power) and power adj saving) and frequency adj (power)   | 18  | 11  | ((375/376.ccls. and ((pll or phase adj  |           | 2003/04/14 11:27    |
| 19   0 (1375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer) and control adj circuits1 and frequency adj locked) or frequency adj synthesizer) and control adj circuits1 and frequency adj locked) or frequency adj synthesizer) and control adj circuits1 and frequency adj synthesizer) and locked) or fr   |     |     | locked) or frequency adj synthesizer)) and                                      |           |                     |
| 19   |     |     | (power adj saving)) and frequency adj   |           |                     |
| locked  or frequency adj synthesizer  and (power adj saving) and frequency adj synthesizer ) and locked  or frequency adj synthesizer ) and locked  or frequency adj synthesizer ) and (power adj saving) and frequency adj synthesizer ) and (power adj saving) and frequency adj divider\$11 and (independently or separately)   21  | _   |     |   |           | 2002/04/14 11.29    |
| (175   176   175   | 19  | 0   |   |           | 2003/04/14 11:20    |
| divider\$1 and set adj (independently or separately)   |     |     | (nower adi saving)) and frequency adi   |           |                     |
| 1   (1375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer) and (power adj saving) and frequency adj synthesizer) and power adj saving) and frequency adj synthesizer) and power adj saving   1   (frequency adj divide; adj rate\$1) and power adj saving   20020191727.URPN.   20020191727.URPN.   20020191727.URPN.   20020191727.URPN.   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:39   2003/04/14 11:59   |     |     | divider\$1) and set adj (independently or                                       |           |                     |
| locked) or frequency adj synthesizer) and (power adj saving) and frequency adj divider\$1) and (independently or separately)   20020191727.URPN.   USPAT   2003/04/14 11:29   2003/04/14 11:31   2003/04/14 11:32   2003/04/14 11:31   2003/04/14 11:31   2003/04/14 11:31   2003/04/14 11:31   2003/04/14 11:32   2003/04/14 11:31   2003/04/14 11:32   2003/04/14 11:32   2003/04/14 11:32   2003/04/14 11:33   2003/04/1  |     |     | separately)   |           |                     |
| Comparison and independently or separately   | 20  | 1   | (((375/376.ccls. and ((pll or phase adj   |           | 2003/04/14 11:28    |
| dividers1) and (independently or separately)   20020191721.URPN.   USPAT   U   | ,   |     | locked) or frequency adj synthesizer)) and                                      |           | •                   |
| Separately   |     |     | divider\$1) and (independently or   |           |                     |
|  |     |     |   |           |                     |
| Dever adj saving   Dever adj saving   Description   Dever adj saving   | 21  | 0   |   |           |                     |
| 296   (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer) and control adj circuit\$1   (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer) and control adj circuit\$1   (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer) and control adj circuit\$1   and frequency adj synthesizer) and control adj circuit\$1   and frequency adj synthesizer) and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj circuit\$1   and frequency adj synthesizer)   and control adj dividing   and control adj circuit\$1   and frequency adj synthesizer)   and control adj dividing   and control adj circuit\$1   adj frequenc\$2   and frequency adj 2 dividing   and control adj core   adj   | 22  | 1   |   | -         | 2003/04/14 11:31    |
| 296   (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer) and control adj circuits1   (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer) and control adj circuits1   (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer) and control adj circuits1 and frequency adj   (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer) and control adj circuits1   ((1375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)   and control adj circuits1   ((1375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)   and control adj circuits1   and frequency adj   ((14912437"   "5325401"   "5335365"   (14912437"   "5325401"   "5335365"   (14912437"   "5325401"   "5335365"   (14912437"   "5325401"   "5335365"   (14912437"   "5325401"   "5335365"   (14912437"   "5325401"   "5335365"   (14912437"   "5325401"   "5413487"   (14912437"   "5413488").PN.     1  |     |     | power adj saving  |           |                     |
| locked  or frequency adj synthesizer)   and control adj circuit\$1   |     |     |   |           |                     |
| Control adj circuit\$1   EPO: JPO: DENNEMT   Control adj circuit\$1   Cocked) or frequency adj synthesizer) and control adj circuit\$1 and frequency adj synthesizer) and control adj circuit\$1 and frequency adj synthesizer) and control adj circuit\$1 and frequency adj locked) or frequency adj synthesizer) and control adj circuit\$1 and frequency adj divid\$3 and set (((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control adj circuit\$1 and frequency adj locked) or frequency adj synthesizer) and control adj circuit\$1 and frequency adj divid\$3 and set   and rate\$1   USPAT; USPA  | 24  | 296 |   |           | 2003/04/14 11:52    |
| DERWENT   Control adj circuit\$1 and frequency adj synthesizer) and control adj circuit\$1 and frequency adj control adj c  |     |     |   |           |                     |
| 131   ((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control adj circuit\$1) and frequency adj locked) or frequency adj synthesizer)) and control adj circuit\$1) and frequency adj locked) or frequency adj synthesizer)) and control adj circuit\$1) and frequency adj locked) or frequency adj synthesizer)) and control adj circuit\$1) and frequency adj locked) or frequency adj synthesizer)) and control adj circuit\$1) and frequency adj divid\$3 and set and rate\$1   |     |     | control adj circuitai   |           |                     |
| locked) or frequency adj synthesizer) and control adj circuit\$1) and frequency adj coked) or frequency adj synthesizer) and control adj circuit\$1 and frequency adj divid\$3 and set control adj circuit\$1 and frequency adj divid\$3 and set control adj circuit\$1 and frequency adj control adj circuit\$1 and frequency adj coked) or frequency adj synthesizer) and control adj circuit\$1 and frequency adj control adj circuit\$1 and frequency adj control adj circuit\$1 and frequency adj control adj control adj circuit\$1 and frequency adj circuit\$1 and frequency adj circuit\$2 and frequency adj  | 25  | 131 | ((375/376.ccls. and ((pll or phase adj  |           | 2003/04/14 11:52    |
| 14   ((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control adj circuit\$1) and frequency adj locked) or frequency adj synthesizer) and control adj circuit\$1) and frequency adj locked) or frequency adj synthesizer) and control adj circuit\$1) and frequency adj divid\$3) and set   USPAT; US   |     |     | locked) or frequency adj synthesizer)) and                                      |           |                     |
| 114  |     |     |   | · ·       |                     |
| locked or frequency adj synthesizer)   and control adj circuit\$1) and frequency adj   divid\$3) and set   ((((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control adj circuit\$1) and frequency adj   DERWENT   USPAT;   US  | 26  | 114 | 1 = · · · ·   |           | 2003/04/14 11:53    |
| Control adj circuit\$1) and frequency adj divid\$3) and set ((((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control adj circuit\$1) and frequency adj synthesizer)   SPAT; US-PCPUB; EPG; JPO; DERWENT USPAT   CO03/04/14 12:35   USPAT   CO03/04/14 12:41   USPAT   USPAT   CO03/04/14 12:41   USPAT   USPAT   CO03/04/14 12:51   USPAT   USPAT   USPAT   CO03/04/14 12:51   USPAT   USPAT   CO03/04/14 12:51   USPAT   CO03/04/1  | 20  | 114 |   |           | 2000, 01, 21 22,00  |
| 27 63 ((((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control adj circuit\$1) and frequency adj synthesizer) DERWENT ("4912437"   "532401"   "5335365"   "5390168"   "5423076"   "5461348").PN. 29 1 6052571.URPN. 30 149 (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control1\$3 adj frequenc\$3 31 37 ((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control1\$3 adj frequenc\$3) and frequency adj 2 dividing ((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control1\$3 adj frequenc\$3) and frequency adj 2 dividing ((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control1\$3 adj frequenc\$3) and frequency adj2 dividing ((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control1\$3 adj frequenc\$3) and frequency adj2 dividing ((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and control1\$3 adj frequenc\$3) and frequency adj2 dividing (and (individual or independently or separately)  33 0 20010036239.URPN. 34 0 6169774.URPN. 35 3 ("5430773"   "5548235"   "5877641").PN. 36 255 phase adj comparator and lock adj detector) 37 19 (phase adj comparator and lock adj detector) 38 ((comparison adj signal\$1) and (comparison adj signal\$1) and (comparison adj signal\$1) and (comparison adj signal\$1) and (comparison adj signal\$1)) and (compariso  |     |     | control adj circuit\$1) and frequency adj                                       |           |                     |
| locked) or frequency adj synthesizer) and control adj circuit\$1) and frequency adj divid\$3) and set) and rate\$1   28  |     |     |   |           | 2002/04/14 11.52    |
| Control adj circuit\$1) and frequency adj divid\$3 and set) and rate\$1  | 27  | 63  | ((((3/5/3/6.ccis. and ((pil or phase ad)  |           | 2003/04/14 11:53    |
| 28   |     |     | control adj circuit\$1) and frequency adj                                       |           |                     |
| 1  |     |     | divid\$3) and set) and rate\$1  | DERWENT   |                     |
| 1 6052571_URPN. 149 (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3  31 37 ((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing 32 7 ((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing 32 7 ((375/376.ccls. and ((pll or phase adj locked) or frequency adj2 synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing) and (individual or independently or separately) 33 0 20010036239_URPN. 34 0 6169774_URPN. 35 3 ("5430773"   "5548235"   "5877641").PN. 36 255 phase adj comparator and lock adj detector) 37 19 (phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1) and (comparison adj signal\$1)) and control adj 38 9 ((phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1)) and control adj 38 P9 ((phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1)) and control adj 39 ((phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1)) and control adj   | 28  | 6   | ("4912437"   "5325401"   "5335365"  | USPAT     | 2003/04/14 12:35    |
| 149   (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$\frac{3}{2}\$ adj frequenc\$\frac{3}{2}\$   (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$\frac{3}{2}\$ adj frequenc\$\frac{3}{2}\$   (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$\frac{3}{2}\$ adj frequenc\$\frac{3}{2}\$ and frequency adj controll\$\frac{3}{2}\$ and frequency adj contr | 20  | 1   |   | IISPAT    | 2003/04/14 12:41    |
| locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3   | 1   |     |   |           |                     |
| 37   (375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing (((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing) and (individual or independently or separately)   33   | 30  |     | locked) or frequency adj synthesizer)) and                                      |           |                     |
| 31 37 ((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing ((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing and (individual or independently or separately)  33 0 20010036239.URPN. USPAT (USPAT) 35 0 6169774.URPN. USPAT (USPAT) 36 0 75430773"   "5548235"   "5877641").PN. USPAT (USPAT) 37 19 (phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1)) and control adj (PD; JPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO   |     |     | controll\$3 adj frequenc\$3   |           | *                   |
| locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing (((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing) and (individual or independently or separately)   33  | 21  | 27  | //275/276 cols and //nll or phase adi   |           | 2003/04/14 12:44    |
| Controll\$3 adj frequenc\$3) and frequency adj2 dividing (((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing) and (individual or independently or separately)   USPAT (USPAT)   USPAT (U   | 21  | 3'  |   |           | 2000, 01, 21 22, 11 |
| 32 7 (((375/376.ccls. and ((pll or phase adj locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing) and (individual or independently or separately)  33 0 20010036239.URPN.  34 0 6169774.URPN.  35 3 ("5430773"   "5548235"   "5877641").PN. phase adj comparator and lock adj detector  36 255 phase adj comparator and lock adj detector  37 19 (phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1)  38 9 ((phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1)) and control adj (SPAT; US-PGPUB; EPO; JPO; DERWENT US-PGPUB; EPO; JPO; DERW   |     |     | controll\$3 adj frequenc\$3) and frequency                                      | EPO; JPO; |                     |
| locked) or frequency adj synthesizer)) and controll\$3 adj frequenc\$3) and frequency adj2 dividing) and (individual or independently or separately)  33   |     |     |   |           | 0000 (04 (14 10 50  |
| Controll\$3 adj frequenc\$3) and frequency adj2 dividing) and (individual or independently or separately)   33   | 32  | 7   |   | 1         | 2003/04/14 12:52    |
| adj2 dividing) and (individual or independently or separately)  33   |     |     | controll\$3 add frequenc\$3) and frequency                                      |           |                     |
| 33 0 20010036239.URPN. 34 0 6169774.URPN. 35 3 ("5430773"   "5548235"   "5877641").PN. 36 255 phase adj comparator and lock adj detector 37 19 (phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1) 38 9 ((phase adj comparator and lock adj detector) and (reference adj signal\$1) and detector) and (reference adj signal\$1) and (comparison adj signal\$1) and (comparison adj signal\$1) and detector) and (reference adj signal\$1) and (comparison adj signal\$1) and (comparis                                    |     |     | adj2 dividing) and (individual or   | 1         |                     |
| 34 0 6169774.URPN. 35 3 ("5430773"   "5548235"   "5877641").PN. 36 255 phase adj comparator and lock adj detector  37 19 (phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1)  38 9 ((phase adj comparator and lock adj detector) and (reference adj signal\$1) and detector) and detector) and (reference adj signal\$1) and detector) and detector de   |     |     | independently or separately)  |           | 2002/04/14 12 52    |
| 35 3 ("5430773"   "5548235"   "5877641").PN. 36 255 phase adj comparator and lock adj detector phase adj comparator and lock adj detector) and (reference adj signal\$1) and phase adj comparator and lock adj detector) phase adj comparator and lock adj detector phase adj comparator and lock adj detector) and (reference adj signal\$1) and detector phase adj comparator and lock adj detector) and (reference adj signal\$1) and detector phase adj comparator and lock adj detector) and (reference adj signal\$1) and detector phase adj comparator and lock adj detector phase adj comparator and   | 1   | ·-  |   |           |                     |
| phase adj comparator and lock adj detector USPAT; US-PGPUB; EPO; JPO; DERWENT US-PGPUB; EPO; JPO; DERWENT USPAT; US-PGPUB; EPO; JPO; USPAT;  |     | _   |   | E.        |                     |
| 37 19 (phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1) and (comparison adj signal\$1) and detector) and (reference adj signal\$1) and detector) and (reference adj signal\$1) and (comparison adj signal\$1) and (comparison adj signal\$1) and (comparison adj signal\$1) and control adj EPO; JPO;  |     | _   | phase adj comparator and lock adj detector                                      |           |                     |
| 19 (phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1) and (phase adj comparator and lock adj detector) and (reference adj signal\$1) and detector) and (reference adj signal\$1) and (comparison adj signal\$1) and (comparison adj signal\$1) and control adj EPO; JPO;  |     |     |   |           |                     |
| (phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1) and (phase adj comparator and lock adj detector) and (reference adj signal\$1) and detector) and (reference adj signal\$1) and (comparison adj signal\$1) and control adj (comparison adj signal\$1) and control adj (comparison adj signal\$1) and control adj  |     |     |   | ł .       |                     |
| detector) and (reference adj signal\$1) and (comparison adj signal\$1) and (comparison adj signal\$1)  ((phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1) and (comparison adj signal\$1) and control adj (comparison adj signal\$1) and (compar                               | 37  | 1 0 | (phase adi comparator and lock adi  | l .       | 2003/04/14 12:54    |
| (comparison adj signal\$1)  ((phase adj comparator and lock adj detector) and (reference adj signal\$1) and (comparison adj signal\$1) and control adj (EPO; JPO; DERWENT USPAT; USPAT; US-PGPUB; EPO; JPO;  | "   |     | detector) and (reference adj signal\$1) and                                     | US-PGPUB; |                     |
| 9 ((phase adj comparator and lock adj detector) and (reference adj signal\$1) and (US-PGPUB; (comparison adj signal\$1)) and control adj EPO; JPO;   |     |     | (comparison adj signal\$1)  | 1         |                     |
| detector) and (reference adj signal\$1) and US-PGPUB; (comparison adj signal\$1)) and control adj EPO; JPO;  | 120 |     | //share add comparator and look add   | l .       | 2003/04/14 13:03    |
| (comparison adj signal\$1)) and control adj EPO; JPO;  | 38  | 9   | ((phase adj comparator and lock adj detector) and (reference adj signal\$1) and |           | 2003/04/14 13.03    |
|  |     |     | (comparison adj signal\$1)) and control adj                                     | l .       |                     |
|  |     |     |   | DERWENT   |                     |

| 39  | 0      | hiroshi adj baba.in.                        | USPAT;    | 2003/04/14 13:04 |
|-----|--------|---|-----------|------------------|
|     |        | -   | US-PGPUB; |                  |
|     |        |   | EPO; JPO; |                  |
|     |        |   | DERWENT   |                  |
| 40  | 0      | hiroshi adj baba                            | USPAT;    | 2003/04/14 13:11 |
|     |        | •   | US-PGPUB; |                  |
|     |        |   | EPO; JPO; |                  |
|     |        |   | DERWENT   |                  |
| 41  | 390360 | hiroshi.in.                                 | USPAT;    | 2003/04/14 13:11 |
| **  |        |   | US-PGPUB; |                  |
|     |        |   | EPO; JPO; |                  |
|     |        |   | DERWENT   |                  |
| 42  | 1181   | hiroshi.in. and baba                        | USPAT;    | 2003/04/14 13:18 |
|     |        |   | US-PGPUB; |                  |
| 1   |        |   | EPO; JPO; |                  |
| }   |        |   | DERWENT   |                  |
| 43  | 1      | (hiroshi.in. and baba) and phase adj        | USPAT;    | 2003/04/14 13:13 |
|     |        | comparator                                  | US-PGPUB; | 1                |
|     |        | -   | EPO; JPO; |                  |
|     |        |   | DERWENT   |                  |
| 44  | 11956  | phase adj comparator with control\$3        | USPAT;    | 2003/04/14 13:28 |
|     |        | •   | US-PGPUB; |                  |
|     |        | ,   | EPO; JPO; |                  |
|     |        |   | DERWENT   |                  |
| 45  | 3894   | (phase adj comparator with control\$3) and  | USPAT;    | 2003/04/14 13:20 |
|     |        | frequency adj divider\$1                    | US-PGPUB; |                  |
|     |        |   | EPO; JPO; |                  |
|     |        |   | DERWENT   |                  |
| 46  | 53     |   | USPAT;    | 2003/04/14 13:21 |
|     |        | frequency adj divider\$1) and power adj     | US-PGPUB; |                  |
|     |        | (sav\$3 or reduc\$4)                        | EPO; JPO; |                  |
| Y . |        |   | DERWENT   |                  |
| 47  | 13     |   | USPAT;    | 2003/04/14 13:21 |
|     |        | and frequency adj divider\$1) and power adj | US-PGPUB; |                  |
|     |        | (sav\$3 or reduc\$4)) and (separately or    | EPO; JPO; |                  |
|     |        | individually)                               | DERWENT   |                  |
| 48  | 20     |   | USPAT;    | 2003/04/14 13:29 |
|     |        | frequency adj2 dividing adj rat\$3 adj      | US-PGPUB; | 1                |
|     |        | setting                                     | EPO; JPO; |                  |
| 1   |        |   | DERWENT   |                  |